IN THE CLAIMS:

Please amend Claims 1 and 3 as shown below.

1. (Currently Amended) A method of controlling a semiconductor device using a microcomputer having a timer, the method comprising:

an interrupting step of interrupting the semiconductor device using the timer, by generating a control signal for controlling a timing of turning on/off the semiconductor device,

wherein the interrupting step is performed in accordance with an interrupting signal which is input to the microcomputer at every occurrence of a predetermined period,

and wherein the interrupting step comprises a setting step of giving a set value to the timer and a calculating step, which follows the setting step, of determining a set value for a subsequent interrupting step, such that the set value determined in the calculating step is given to the timer in the setting step of the subsequent interrupting step.

2. (Previously Presented) The method of controlling the semiconductor device according to claim 1, wherein the setting step of the interrupting step sets a first timing of turning on/off the semiconductor device, the first timing being calculated in the calculating step of a preceding interrupting step,

and wherein the calculating step of the interrupting step calculates a second timing of turning on/off the semiconductor device, the second timing being set for the timer in the setting step of the subsequent interrupting step.

3. (Currently Amended) A method of controlling at least first and second semiconductor devices using a microcomputer having a timer, the method comprising:

an interrupting step of interrupting the at least first and second semiconductor devices using the timer, by generating a control signal for controlling a timing of turning on/off the at least first and second semiconductor devices,

wherein the interrupting step is performed in accordance with an interrupting signal which is input to the microcomputer at every occurrence of a predetermined period,

and wherein the interrupting step comprises a setting step of giving a set value to the timer and a calculating step, which follows the setting step, of determining a set value for a subsequent interrupting step, such that the set value determined in the calculating step is given to the timer in the setting step of the subsequent interrupting step.

4. (Previously Presented) The method of controlling the semiconductor device according to any one of claims 1 to 3, wherein the semiconductor device of a power converter is controlled.

5. (Previously Presented) The method of controlling the semiconductor device according to claim 4, wherein the power converter is a power conditioner for photovoltaic power generation.

6. (Previously Presented) A computer-executable program stored on a computer readable medium, the program for performing the method of controlling the semiconductor device according to any one of claims 1 to 3.

7. (Previously Presented) A computer-executable program stored on a computer readable medium, the program for performing the method of controlling the semiconductor device according to claim 4.

8. (Previously Presented) A computer readable medium which stores a computer-executable program for performing the method of controlling the semiconductor device according to any one of claims 1 to 3.

9. (Previously Presented) A computer readable medium which stores a computer-executable program for performing the method of controlling the semiconductor device according to claim 4.